INPUT SIGNAL

DEFECT MAP 26

CORRECTION ELECTRONICS 24

DRIVE ELECTRONICS 22

SYNCHRONOUS DETECTOR 30, 32

ADC 34

μP 28

16

14

10

12

18

20

FIG. 1

OUTPUT

31

33

31

31

S_1

S_2
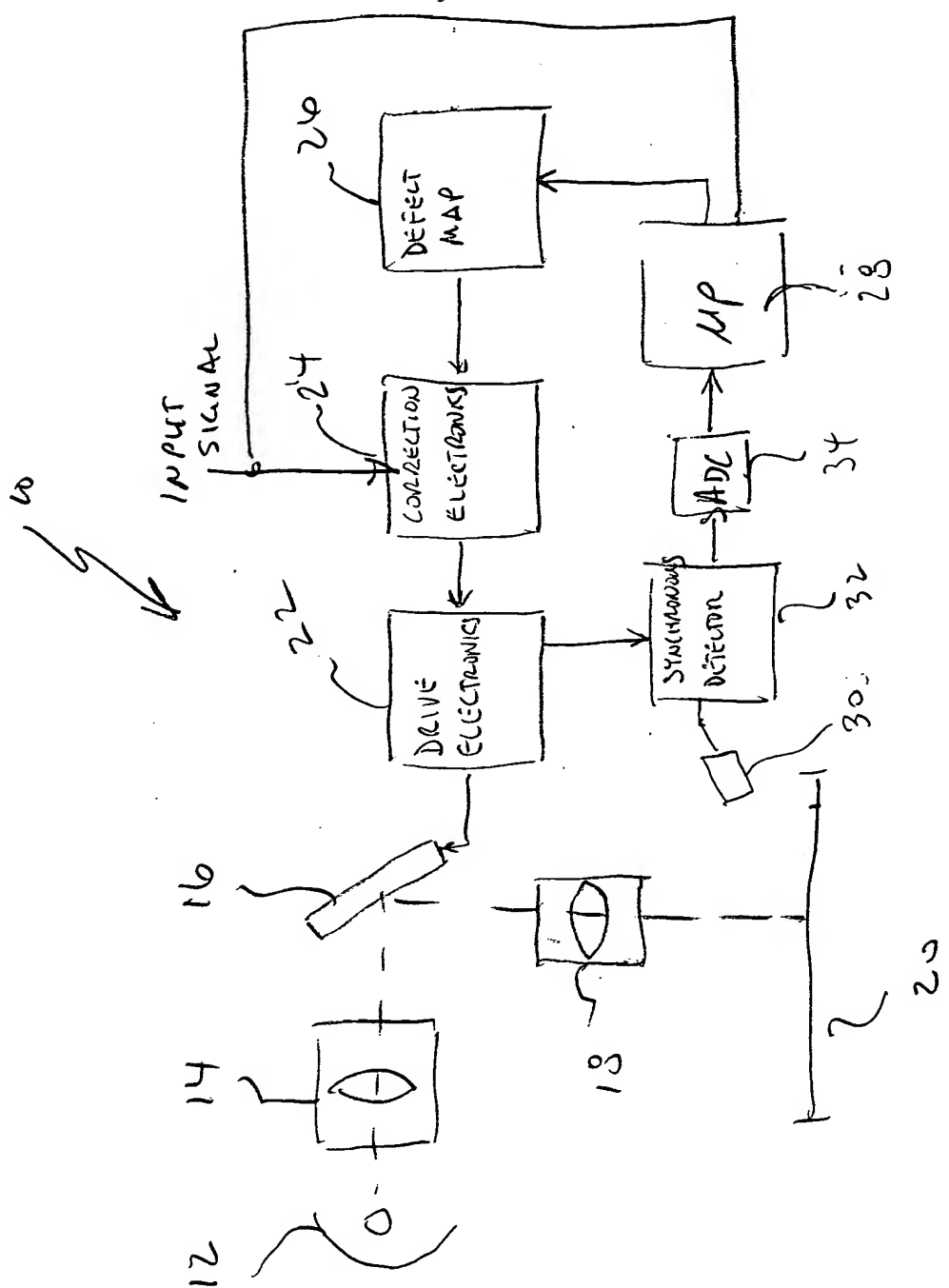
35

$G = S_1/S_2$

INPUT

FIG 2.

| CREATE DEFECT MAP | ~36 |

↓

| CORRECT INPUT SIGNAL ACCORDING TO MAP | ~38 |

↓

| DISPLAY CORRECTED INPUT SIGNAL | ~40 |

FIG. 3

| MODULATE PIXEL AT PREDETERMINED RATE | ~42 |

↓

| SENSE DISPLAY WITH PHOTO SENSOR | ~44 |

↓

| DEMODULATE SENSED SIGNAL WITH SYNCHRONOUS DEMODULATION | ~46 |

↓

| GENERATE CORRECTION | ~48 |

↓

| REPEAT FOR ALL PIXELS ON DISPLAY | ~50 |

FIG. 4